



JFW

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Docket No.: 241160US2S

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

ATTORNEYS AT LAW

ECKHARD H. KUESTERS
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RE: Application Serial No.: 10/632,822

Applicants: Yoshinori MATSUBARA

Filing Date: August 4, 2003

For: SEMICONDUCTOR DEVICE, SEMICONDUCTOR
DEVICE MANUFACTURING METHOD, AND
SEMICONDUCTOR DEVICE TEST METHOD

Group Art Unit: 2815

Examiner: Clark, S.

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of **\$0.00** is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

**OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.**

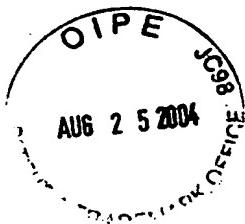
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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

YOSHINORI MATSUBARA

: EXAMINER: CLARK, S.

SERIAL NO: 10/632,822

:

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: GROUP ART UNIT: 2815

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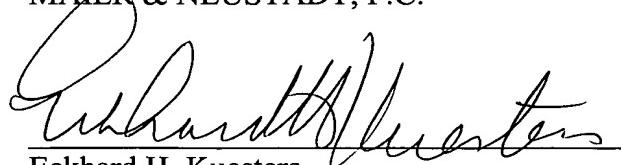
SIR:

In response to the Election Requirement dated July 29, 2004, Applicant elects without traverse Group I, Claims 1-11, drawn to a semiconductor device, classified in class 257, subclass 666+.

Accordingly, examination on the merits of Claims 1-11 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



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